

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,064,358 B2
APPLICATION NO. : 10/743596
DATED : June 20, 2006
INVENTOR(S) : Indrajit Manna et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, line 15, please replace "p-well" with -- N-well -- before "functioning".

Col. 8, line 53, add -- ; -- after "a resistor and said p-doped region".

Col. 8, line 58, please replace "grounded-sate" with -- grounded-gate -- before "nMOS".

Col. 9, line 16, please add -- pad -- after "first I/O".

Col. 9, line 22, please replace "p-well" with -- N-well -- after "the deep".

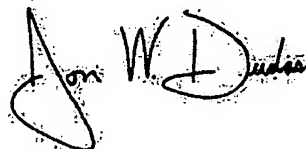
Col. 9, line 50, please replace "fanning" with -- forming -- before "a RC network".

Col. 10, line 24, please replace "p-well" with -- N-well -- before "functioning".

Col. 11, line 4, please replace "p-well" with -- N-well -- before "functioning".

Signed and Sealed this

Second Day of January, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office